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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/536,452	03/28/2000	Ronny Ronen	02207/8754	5160
23838	7590	04/13/2004		EXAMINER
KENYON & KENYON 1500 K STREET, N.W., SUITE 700 WASHINGTON, DC 20005			HUISMAN, DAVID J	
			ART UNIT	PAPER NUMBER
			2183	
			DATE MAILED: 04/13/2004	

18

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Advisory Action</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	09/536,452	RONEN ET AL.
	<b>Examiner</b> David J. Huisman	<b>Art Unit</b> 2183

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 02 April 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY [check either a) or b)]**

- a)  The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.
- b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1.  A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2.  The proposed amendment(s) will not be entered because:
  - (a)  they raise new issues that would require further consideration and/or search (see NOTE below);
  - (b)  they raise the issue of new matter (see Note below);
  - (c)  they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d)  they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_.

3.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
4.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5.  The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: see attached sheet.
6.  The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7.  For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_.

Claim(s) objected to: \_\_\_\_\_.

Claim(s) rejected: \_\_\_\_\_.

Claim(s) withdrawn from consideration: \_\_\_\_\_.

8.  The drawing correction filed on 11 July 2003 is a) approved or b) disapproved by the Examiner.

9.  Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s). \_\_\_\_\_.

10.  Other: \_\_\_\_\_.

On page 7 of the remarks, applicant argues the novelty of claim 1, in substance that:

"...Instead, Killian et al. uses separate "sign extension hardware" and "zeroing circuitry" to sign-extend and zero-extend, respectively, the output address."

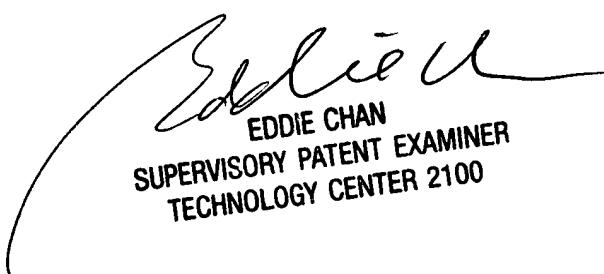
The examiner asserts that the applicant is arguing limitations that are not recited in claim 1. Applicant's claim 1 makes no mention of sign and zero-extend circuitry not being separate.

In addition, on page 7 of the remarks, applicant argues:

"...The zeroing circuitry is operating on a full N-bits and not the "truncated generated address references."

This argument has been fully considered but is deemed non-persuasive for the following reason:

In Fig.5D of Killian, the 64-bit virtual address is truncated (i.e., the upper 32-bits are stripped from the 64-bit address). Then, when in 32-bit user mode, multiplexer 172 will output 32 0's which are eventually appended to the truncated address.

  
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